



Daffodil International University
Department of Software Engineering
Faculty of Science & Information Technology



Quiz_2 (SE222_Computer Architecture)_43 K (Fall-2025)

ID: .

15

Question 1.(CLO2)		Marks
Illustrate the Multiple Interrupt handling of 3 I/O devices with Diagrams and mention its importance.		5
Question 2. (CLO2)		
Demonstrate the Elements of Bus Design.		4
Question 3. (CLO2)		
Find the Hypothetical Processor Instructions from the following generated figure.		6
201	2806	Here, 8421 $0010 \rightarrow$ load AC from memory $0100 \rightarrow$ store AC to memory $0101 \rightarrow$ Subtract to AC from memory
202	4807	
203	5808	
806	0007	
807	0005	
808	0003	
809	0008	

Ans: to the que: No: 1

The Multiple Interrupt handling of 3 I/O device:-