



Daffodil International University
Department of Software Engineering
Faculty of Science & Information Technology

Quiz_2 (SE222_Computer Architecture)_431 (Fall-2025)

ID: 242-3- [redacted]



Question 1. (CLO2)

Illustrate the top level components of GP processor with Diagrams.

15

Question 2. (CLO2)

Demonstrate the instruction cycle state diagram without and with interrupt.

Marks
4

Question 3. (CLO2)

Find the Hypothetical Processor Instructions from the following generated figure.

5
6

201	3806	Here, 0011 → load AC from memory 0100 → store AC to memory 0110 → Subtract to AC from memory
202	6807	
203	7808	
204	4809	
.....		
806	0007	
807	0005	
808	0003	
809	0008	