

Daffodil International University

CSE 223 | Digital Logic Design

Spring 2025

Quiz-3

Name:

ID:

Time: **30 mins**

Date:

Section: 65\_E

Obtained Marks:

/15

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\*Answer all the questions. Use both sides of the paper to write your answer. No extra pages will be provided.\*

1. Construct a 16 to 1 multiplexer with 4 to 1 multiplexers. Give explanation [8]

2. Implement the following Boolean function with a 4 to 1 multiplexer. Give explanation. [7]

$$F(A, B, C) = \sum (4, 5, 6, 7)$$